TABLE VI
INTERFACE CONNECTOR PIN ASSIGNMENTS

PIN	FUNCTION	SIG NAME	COMMENTS
A	SPEED 1/CLOCK +	S1/CK IN+	EXTERNAL DATA CLOCK INPUT:
B	SPEED 1/CLOCK -	S1/CK IN-	
C	SPEED 2 +	S2+	(REM SPEED SELECT BITS 2&4
D	SPEED 2 -	S2-	ARE REDUNDANT WHEN
E	SPEED 4 +	S4+	USING EXT CLOCK INPUTS.)
F	SPEED 4 -	S4-	
G	DATA 1 +	D1+	DATA BITS
H	DATA 1 -	D1-	
J	DATA 2 +	D2+	
K	DATA 2 -	D2-	
L	DATA 4 +	D4+	
M	DATA 4 -	D4-	
N	DATA 8 +	D8+	
P	DATA 8 -	D8-	
R	DATA 16 +	D16+	
S	DATA 16 -	D16-	
T	DATA 32 +	D32+	
U	DATA 32 -	D32-	
V	KEY PULSE +	ZP+	KEY PULSE OUTPUT
W	KEY PULSE -	ZP-	
X	CLOCK OUT +	CKOUT+	CLOCK OUTPUT:
Y	CLOCK OUT -	CKOUT-	
Z	REM RAPID -	CCK/RPD-	REMOTE RAPID ADVANCE.
a	REM RAPID +	CCK/RPD+	
b	GROUND	GROUND	
c	GROUND	GROUND	
d	TRIGGER/INHIBIT -	INH/TRG-	EXTERNAL TRIGGER INPUT OR
e	TRIGGER/INHIBIT +	INH/TRG+	INHIBIT (INT TRG SELECTED)
f	GROUND	GROUND	
g	NOT USED	SPARE	
h	NOT USED	SPARE	
i	NOT USED	SPARE	
j	NOT USED	SPARE	
k	NOT USED	SPARE	
m	NOT USED	SPARE	
n	NOT USED	SPARE	
p	NOT USED	SPARE	
q	NOT USED	SPARE	
r	NOT USED	SPARE	
s	NOT USED	SPARE	
t	NOT USED	SPARE	

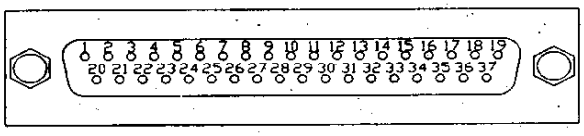
accomplished with systems using a parallel digital interface.

All signals on this interface use differential line drivers and receivers meeting EIA RS-422 standards. Many suitable replacement parts are available, such as the 75731 and 75732 types. Because this interface is intended to connect to only one recorder at a time, it is not necessary to use drivers with tri-state capability.

This **recorder is not equipped with terminating resistors** . If line reflections on long cables present a problem, the user should add parallel or series termination's, as required.

Any reference to the polarity of an interface signal being "high" or "low" refers to the polarity of the voltage on the "+" wire; it is understood that the opposite polarity will be seen on the "-" wire.

J902	FUNCTION	SIG. NAME
1	SPEED1/CLOCK +	S1/CK IN +
2	SPEED1/CLOCK -	S1/CK IN -
3	SPEED 2 +	S2 +
4	SPEED 2 -	S2 -
5	SPEED 4 +	S4 +
6	SPEED 4 -	S4 -
7	DATA 1 +	D1 +
8	DATA 1 -	D1 -
9	DATA 2 +	D2 +
10	DATA 2 -	D2 -
11	DATA 4 +	D4 +
12	DATA 4 -	D4 -
13	DATA 8 +	D8 +
14		
15	DATA 16 +	D16+
16	DATA 16 -	D16-
17	DATA 32 +	D32+
18	DATA 32 -	D32-
19		
20	DATA 8 -	D8 -
21	KEY PULSE -	ZP -
22	KEY PULSE +	ZP +
23	CLOCK OUT +	CKOUT +
24	CLOCK OUT -	CKOUT -
25	REM RAPID+	CCK/RPD+
26	REM RAPID -	CCK/RPD -
27	GROUND	GROUND
28	GROUND	GROUND
29	TRIGGER/INHIBIT-	INH/TRIG -
30	TRIGGER/INHIBIT+	INH/TRIG +
31		+5 VOLTS
32		REM CHART
33	GROUND	GROUND
34		AUX OUT 2+
35		AUX OUT 2-
36		AUX OUT3+
37		AUX OUT3-



2) Analog Interface - The 15 pin analog interface connects to the analog interface board.

There are three BNC connectors which run parallel to the the pins that are located on the rear connector plate. They are; VIDEO (pin 15 Analog Input) SPEED (pins 9, 10

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TABLE VI
INTERFACE CONNECTOR PIN ASSIGNMENTS

PIN	FUNCTION	SIG. NAME	COMMENTS
1	SPEED1/CLOCK +	S1/CK IN +	EXTERNAL DATA CLOCK INPUT:
2	SPEED1/CLOCK -	S1/CK IN -	
3	SPEED 2 +	S2 +	(REM SPEED SELECT BITS 2& 4
4	SPEED 2 -	S2 -	ARE REDUNDENT WHEN
5	SPEED 4 +	S4 +	USING EXTERNAL CLOCK INPUTS)
6	SPEED 4 -	S4 -	
7	DATA 1 +	D1 +	
8	DATA 1 -	D1 -	
9	DATA 2 +	D2 +	
10	DATA 2 -	D2 -	
11	DATA 4 +	D4 +	
12	DATA 4 -	D4 -	
13	DATA 8 +	D8 +	
14	DATA 8 -	D8 -	
15	KEY PULSE -	ZP -	KEY PULSE OUTPUT:
16	KEY PULSE +	ZP +	ABOUT 50 µSEC.
17	CLOCK OUT +	CKOUT +	CLOCK OUTPUT:
18	CLOCK OUT -	CKOUT -	8000 PULSES PER SWEEP.
19	REM RAPID -	CCK/RPD-	REMOTE RAPID ADVANCE.
20	REM RAPID +	CCK/RPD +	
21	GROUND	GROUND	
22	GROUND	GROUND	
23	TRIGGER/INHIBIT +	INH/TRIG +	EXTERNAL TRIGGER INPUT OR
24	TRIGGER/INHIBIT -	INH/TRIG -	INHIBIT (INT TRG SELECTED)
25	NOT USED	SPARE	

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TABLE II
INTERFACE CONNECTOR PIN ASSIGNMENTS

FROM	TO	AWG	COLOR	DESCRIPTION
P1-1	J1-1	24	RED	STROBE
P1-2	J1-17	24	ORG/WHT	D0
P1-3	J1-15	24	YEL/WHT	D1
P1-4	J1-13	24	BLU/WHT	D2
P1-5	J1-11	24	PURPLE	D3
P1-6	J1-9	24	GRAY	D4
P1-7	J1-7	24	YELLOW	D5
P1-8	J1-5	24	RED/WHT	D6
P1-9	J1-3	24	GREEN	D7
P1-10	J1-19	24	BLK/WHT	-ACK
P1-11	J1-21	24	BROWN	BUSY
P1-12	J1-23	24	WHITE	PAPER
P1-13	J1-25	24	BLACK	SELECT
P1-14	J1-2	24	BLU/BLK/WHT	-AUTO LF
P1-15	J1-4	24	BLK/YEL/WHT	-ERROR
P1-16	J1-6	24	GRN/WHT	INIT
P1-17	J1-8	24	BLK/BRN/WHT	-SELECT IN
P1-18	J1-10	24	PUR/WHT	GROUND
P1-19	J1-12	24	GRY/WHT	GROUND
P1-20	J1-14	24	BLK/ORG/WHT	GROUND
P1-21	J1-16	24	BRN/WHT	GROUND
P1-22	J1-18	24	BRN/BLK/WHT	GROUND
P1-23	J1-20	24	BLK/GRN/WHT	GROUND
P1-24	J1-22	24	ORANGE	GROUND
P1-25	J1-24	24	BLUE	GROUND

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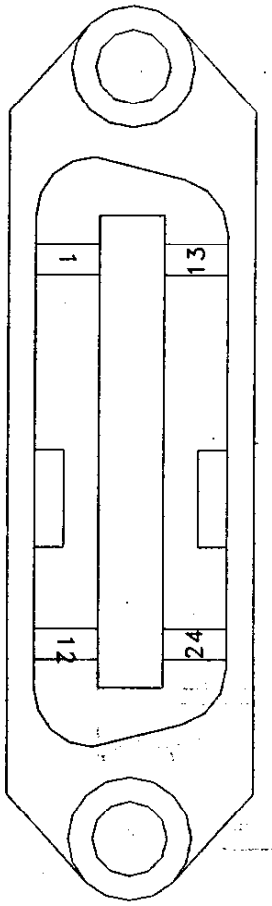

PIN	SIGNAL NAME
1	DI01
2	DI02
3	DI03
4	DI04
5	E01
6	DAV
7	NRFD
8	NDAC
9	IFC
10	SRQ
11	ATN
12	SHIELD
13	DI05
14	DI06
15	DI07
16	DI08
17	REN
18	GND6
19	GND7
20	GND8
21	GND9
22	GND10
23	GND11
24	LOGIC GND

Table II - GPIB Pin Outs

Figure 6 - Connector Pin Outs

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B.4 Parallel Printer Port Pin-out:
(say that five times fast)

Model 9800 'Parallel' Connector	Description
1	STROBE
2	D0
3	D1
4	D2
5	D3
6	D4
7	D5
8	D6
9	D7
10	ACK
11	BUSY
12	PAPER
13	SELECT
14	AUTO LF
15	ERROR
16	INIT
17	SELECT IN
18	GROUND
19	GROUND
20	GROUND
21	GROUND
22	GROUND
23	GROUND
24	GROUND
25	GROUND